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**1 MPEG-4 rate control for multiple video objects**
*Vetro, A.; Huifang Sun; Yao Wang;*

Circuits and Systems for Video Technology, IEEE Transactions on , Volume: 9 , Issue: 1 , Feb. 1999  
 Pages:186 - 199

[\[Abstract\]](#)    [\[PDF Full-Text \(480 KB\)\]](#)    **IEEE JNL**
**2 Optimizing the stage resolution in pipelined, multistage, analog-to-digital converters for video-rate applications**
*Lewis, S.H.;*

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Vol 39 , Issue: 8 , Aug. 1992  
 Pages:516 - 523

[\[Abstract\]](#)    [\[PDF Full-Text \(700 KB\)\]](#)    **IEEE JNL**
**3 Improving MPEG-4 coding performance by jointly optimising compression and blocking effect elimination**
*Cheung, W.-F.; Chan, Y.-H.;*

Vision, Image and Signal Processing, IEE Proceedings- , Volume: 148 , Issue: 3 , June 2001  
 Pages:194 - 201

[\[Abstract\]](#)    [\[PDF Full-Text \(676 KB\)\]](#)    **IEEE JNL**
**4 Minimizing blocking probability in a hierarchical storage based VOD server**
*Won, Y.; Srivastava, J.;*

Multimedia Database Management Systems, 1996., Proceedings of International Workshop on , 14-16 Aug. 1996  
Pages:12 - 19

[\[Abstract\]](#)   [\[PDF Full-Text \(620 KB\)\]](#)   IEEE CNF

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**5 An effective approach to video staging in streaming applications**

*Shin-Hung Chang; Ray-I Chang; Jan-Ming Ho; Yen-Jen Oyang;*  
Global Telecommunications Conference, 2002. GLOBECOM '02. IEEE , Volume 2 , 17-21 Nov. 2002  
Pages:1733 - 1737 vol.2

[\[Abstract\]](#)   [\[PDF Full-Text \(413 KB\)\]](#)   IEEE CNF

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**6 Real time low bit-rate video coding algorithm using multi-stage hierarchical vector quantization**

*Terada, K.; Takeuchi, M.; Tamaru, K.;*  
Acoustics, Speech, and Signal Processing, 1998. ICASSP '98. Proceedings of the 1998 IEEE International Conference on , Volume: 5 , 12-15 May 1998  
Pages:2673 - 2676 vol.5

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**7 A new object-oriented approach for video compression at very low bit rate**

*LeQuang, D.; Zaccarin, A.;*  
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Pages:823 - 826 vol.2

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**8 A pipelined 9-stage video-rate analog-to-digital converter**

*Lewis, S.H.; Fetterman, H.S.; Gross, G.F., Jr.; Ramachandran, R.; Viswanath T.R.;*  
Custom Integrated Circuits Conference, 1991., Proceedings of the IEEE 1991 15 May 1991  
Pages:26.4/1 - 26.4/4

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**9 Frame-rate adaptation and bi-directional coding for video sequence**

*Alexandre, B.; Laurent, N.;*  
MPEG-4. 2001 Proceedings of Workshop and Exhibition on , 18-20 June 2001  
Pages:99 - 102

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**10 Architecture of a high speed Reed-Solomon decoder**

*Iwaki, T.; Tanaka, T.; Yamada, E.; Okuda, T.; Sasada, T.;*  
Consumer Electronics, IEEE Transactions on , Volume: 40 , Issue: 1 , Feb. 1992  
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**11 Enhanced interframe coding based on morphological segmentation**  
*Czerepinski, P.J.; Bull, D.R.;*  
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4 , Aug. 1997  
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**12 Error-robust video coding with channel-optimized trellis-coded quantization**  
*Liu, Z.; Abousleman, G.P.; Karam, L.J.;*  
Signals, Systems and Computers, 2000. Conference Record of the Thirty-Four  
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**13 Robust face detection at video frame rate based on edge orientation features**  
*Froba, B.; Kublbeck, C.;*  
Automatic Face and Gesture Recognition, 2002. Proceedings. Fifth IEEE  
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**14 An efficient low-bit rate adaptive mesh-based motion compensation technique**  
*Mahmoud, H.A.; Bayoumi, M.A.;*  
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**15 TSFD: two stage frame dropping for scalable video transmission over data networks**  
*Bing Zheng; Atiquzzaman, M.;*  
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1	Method and apparatus for distributing a clock in a network	710/58	713/400; 713/500; 713/501	
2	Communications device, method thereof, communications system and recording medium	370/466	370/395.6; 370/395.64; 370/465	
3	Use of a circular buffer to assure in-order delivery of packets	370/412	370/394; 370/428; 370/429	
4	SCSI data rate speed determination	710/107	710/33; 710/60	
5	Architecture enabling code overlay using a dedicated endpoint	710/310	710/104; 713/100	
6	Information detecting circuit including adaptive equalizer and reproducing apparatus	369/59.22	369/124.07; 369/47.18	
7	Non-linear flash and lightning detection device	396/263	396/267; 73/170.24	
8	Adaptive control of streaming data in a graph	713/600	713/400	
9	Electronic imaging apparatus	358/402	358/479	
10	Transmitting system and transmitting apparatus	386/111	348/423.1	
11	Architecture using dedicated endpoints and protocol for creating a multi-application interface and improving bandwidth over universal serial bus	710/305	710/306	
12	Communication apparatus which handles a time stamp	713/600	713/400; 713/503	
13	Power control method and apparatus therefor	713/320		
14	Computer system and method for providing digital video and data over a communication channel	707/10	725/109	
15	System management mode circuits, systems and methods	710/261	710/262; 710/263; 710/266; 710/305	
16	High speed encoding and decoding methods for analog subscriber connections	375/242	375/245; 375/295	
17	Computer system with adaptive heartbeat	714/23	709/201	

	Title	Current OR	Current XRef	Retrieval Classif
18	Communication apparatus, communication method and storage medium	375/240.28		
19	Computer controlled display system using a graphical replay device to control playback of temporal data representing collaborative activities	715/500.1	715/512	
20	System and method for selective capture of video frames	348/699	348/448; 348/571	
21	Bus management card for use in a system for bus monitoring	714/56	710/301; 713/501; 714/48	
22	Wide tracking range, auto ranging, low jitter phase lock loop for swept and fixed frequency systems	327/156	375/376	
23	Recording and/or replaying teletext signals	386/95	386/46	
24	Multi-service circuit for telecommunications	370/395.43	370/395.5; 370/465; 370/535	
25	Adaptive digital clock recovery	370/235	370/356; 370/516; 375/371	
26	High speed communications system for analog subscriber connections	375/242	375/254; 375/285	
27	High speed communications system for analog subscriber connections	375/231	375/242	
28	Optical reader with adaptive exposure control	235/462.06	235/462.25	
29	Metropolitan area network switching system and method of operation thereof	370/401	370/535; 398/9	
30	System and method for maintaining timing synchronization in a digital video network	370/503	370/498	
31	Synchronous/asynchronous data detection apparatus for use in a magnetic recording/playback system	375/233	360/51; 360/65; 375/232; 375/341; 375/346	
32	Comb filtered signal separation	348/663	348/665; 348/668	

	Title	Current OR	Current XRef	Retrieval Classif
33	3-brain architecture for an intelligent decision and control system	706/23	706/15; 706/16; 706/26; 706/27	
34	System and method for the delivery of digital video and data over a communication channel	725/114	725/109; 725/87	
35	Method for synchronization of a clock signal, which can be generated in a data receiving station, with a clock signal which is used in a data transmission station	375/354	370/509; 709/400	
36	Ultrasound imaging system and method employing an adaptive filter	600/443		
37	Simulcasting digital video programs for broadcast and interactive services	370/522	370/535; 455/3.03; 725/87; 725/91	
38	Method and apparatus for handling system management interrupts (SMI) as well as, ordinary interrupts of peripherals such as PCMCIA cards	710/260	710/261; 710/269; 710/302	
39	Low jitter timing recovery technique and device for asynchronous transfer mode (ATM) constant bit rate (CBR) payloads	370/395.62	370/252; 370/395.64; 370/516; 370/518; 375/372; 375/376	
40	Electronic imaging apparatus	358/442	358/468; 379/100.01; 379/100.12	
41	AAL-5 SSCS for AAL-1 and AAL-2 in ATM networks	370/395.6	370/395.61; 370/395.65; 370/469; 370/474; 714/52; 714/807	
42	Point-to-point interconnect communications utility	709/233	709/228; 709/237; 709/248; 714/12	
43	Method and apparatus for modified baud rate sampling	375/233	375/350; 708/323	

	Title	Current OR	Current XRef	Retrieval Classif
44	Adaptive signal processing method and circuit for a digital recording/reproducing apparatus	360/51	360/65	
45	Timing and control for deinterlacing and enhancement of non-deterministically arriving interlaced video data	348/448	348/452	
46	Power management masked clock circuitry, systems and methods	713/500	713/601	
47	Digital entertainment terminal providing dynamic execution in video dial tone networks	345/716	725/119; 725/31	
48	High speed communications system for analog subscriber connections	375/340	375/222	
49	Interrupt routing circuits, systems and methods	710/48	710/260	
50	Electronic imaging apparatus	358/402	358/404; 358/474; 358/483	
51	System and method for maintaining network synchronization utilizing digital phase comparison techniques with synchronous residual time stamps	375/372	370/516; 375/364	
52	Method and apparatus using GPS to reshape isochronous data at the receiving ends of an ATM network	370/230.1	370/517	
53	Structure and method of performing DMA transfers between memory and I/O devices utilizing a single DMA controller within a notebook and docking station computer system	710/303	710/308	
54	Power management interface system for use with an electronic wiring board article of manufacture	713/300	710/100	
55	Quadrilateral multichip computer systems and printed circuit boards therefor	712/32	257/499; 361/780; 713/300	



	Title	Current OR	Current XRef	Retrieval Classif
56	Systems, circuits and methods for mixed voltages and programmable voltage rails on integrated circuits	713/324		
57	Dynamic system clocking and address decode circuits, methods and systems	713/323	712/32; 712/36; 713/300; 713/324; 713/330	
58	Computer system power management interconnection circuitry, systems and methods	713/320	713/300	
59	High speed communications system for analog subscriber connections	375/242	370/523; 375/285	
60	Terminal for receiving simulcast digital video programs	370/537	348/21; 375/130	
61	Integrated circuits for low power dissipation in signaling between different-voltage on chip regions	326/81	326/68	
62	Computer system and an electronic circuit utilizing a single DMA controller and additional communication circuit to manage DMA transfers between memory and I/O devices	710/308	710/22	
63	Computer system power management interconnection circuitry, system and methods	710/260	710/48; 713/300	
64	Clock control circuits, systems and methods	713/500	713/601	
65	Method and apparatus for implementing a single DMA controller to perform DMA operations for devices on multiple buses in docking stations, notebook and desktop computer system	710/303	361/683	
66	High speed communications system for analog subscriber connections	375/295	375/242; 375/319	
67	Digital entertainment terminal providing dynamic execution in video dial tone networks	725/134	725/114	

	Title	Current OR	Current XRef	Retrieval Classif
68	Split data path fast at-bus on chip circuits systems and methods	710/315	710/20	
69	Simulcasting digital video programs for broadcast and interactive services	370/487	370/522	
70	SRTS clock recovery system implementing adaptive clock recovery techniques	375/372	370/519; 375/364	
71	High performance microprocessor having variable speed system clock	710/25		
72	High speed communications system for analog subscriber connections	375/254	375/222	
73	High speed communications system for analog subscriber connections	375/340	375/231; 375/242	
74	Wireless video distribution	725/81	725/118; 725/131; 725/85	
75	Computer controlled display system activities using correlated graphical and timeline interfaces for controlling replay of temporal data representing collaborative activities	345/720	345/723; 345/804	
76	Shared use video processing systems for distributing program signals from multiplexed digitized information signals	725/73	370/487; 725/116; 725/117; 725/122; 725/131	
77	CPU, memory controller, bus bridge integrated circuits, layout structures, system and methods	716/10	712/32; 716/1; 716/8	
78	Power management supply interface circuitry, systems and methods	713/300	713/320; 713/322; 713/323; 713/330	
79	Power management masked clock circuitry, systems and methods	713/500	713/501	
80	Clock control circuits, systems and methods	713/500	713/322; 713/503; 713/601	
81	Apparatus and method for controlling point-to-point interconnect communications between nodes	709/233	709/237; 709/238; 709/248; 714/12	

	Title	Current OR	Current XRef	Retrieval Classif
82	Adaptive power management processes, circuits and systems	713/300	700/286; 702/60; 710/18; 713/322	
83	AIN interaction through wireless digital video network	370/384	370/522; 379/93.23	
84	SRTS clock recovery system for use in a highly stressed network environment	375/371	370/508; 370/516; 375/226; 375/356	
85	Asynchronous transfer mode (ATM) cell arrival monitoring system	370/394	370/473; 370/474	
86	Systems, circuits and methods for mixed voltages and programmable voltage rails on integrated circuits	713/300	365/185.13; 365/63	
87	Digital entertainment terminal with channel mapping	345/716	725/117; 725/126; 725/132	
88	Power management masked clock circuitry, systems and methods	713/500	713/501	
89	Simulcasting digital video programs for broadcast and interactive services	370/522	370/535; 725/131; 725/67; 725/68; 725/73	
90	Computer system power management interconnection circuitry and systems	713/310	710/260; 713/321; 713/323	
91	Power management supply interface circuitry, systems and methods	713/300	307/31; 307/66	
92	System for the capture and replay of temporal data representing collaborative activities	345/716	345/751; 345/856; 707/2	
93	Computer controlled display system using a timeline to control playback of temporal data representing collaborative activities	345/716	345/751; 707/2	
94	Electronic imaging apparatus	358/402	358/1.15; 358/404; 358/444	
95	Clock control circuits, systems and methods	713/500	713/322; 713/503; 713/601	

	Title	Current OR	Current XRef	Retrieval Classif
96	Integrated circuit design for handling of system management interrupts (SMI)	710/260	710/261	
97	Network providing signals of different formats to a user by multiplexing compressed broadband data with data of a different format into MPEG encoded data stream	709/246	370/395.64; 370/395.65; 375/240.25; 382/232	
98	Simulcast transmission of digital programs to shared antenna receiving systems	370/486	348/614; 370/537	
99	Simultaneous overlapping broadcasting of digital programs	370/537	348/614; 375/240.25	
100	Rapid-update adaptive channel-equalization filtering for digital radio receivers, such as HDTV receivers	375/232	348/607; 708/323	
101	Simulcast transmission of digital programs to shared antenna receiving systems	370/312	375/240.01; 375/347	
102	Customer premise wireless distribution of broad band signals and two-way communication of control signals over power lines	725/116	340/310.06; 370/342; 725/131; 725/79; 725/81	
103	Analog memory system having an integrated circuit frequency domain processor	365/45	365/183	
104	Analog memory system having a frequency domain transform processor	365/45	365/189.01	
105	Analog memory system storing and communicating frequency domain information	365/45	365/189.01	
106	Coupling of voice and computer resources over networks	370/389	370/260; 379/159	
107	Shared receiving systems utilizing telephone cables as video drops	370/487	725/106; 725/122; 725/123; 725/126; 725/99	
108	Moving picture coding apparatus	348/14.1	348/14.13; 348/14.15; 348/14.16	
109	Picture coding and decoding apparatus using vector quantization	1/1		

	Title	Current OR	Current XRef	Retrieval Classif
110	Optical system having an analog image memory, an analog refresh circuit, and analog converters	365/45	365/222	
111	Method of upgrading the program transport capacity of an RF broadcast channel	370/487	370/477; 375/240.01; 725/73	
112	Simulcasting digital video programs	370/517	370/535; 375/240.25; 375/356; 455/503	
113	Self-time processor with dynamic clock generator having plurality of tracking elements for outputting sequencing signals to functional units	713/500		
114	System and method for prefetching information in a processing system	711/119		
115	ATM packet demultiplexer for use in full service network having distributed architecture	370/397	370/474; 725/119; 725/129; 725/138; 725/140; 725/152	
116	Adaptive data rate modem	375/222	375/265; 375/376; 375/377	
117	Adaptive equalizing apparatus for controlling the input signal level of quantized feedback	375/233	333/17.2; 333/18; 375/232; 375/346; 375/350; 708/323	
118	High speed lossless data compression method and apparatus using side-by-side sliding window dictionary and byte-matching adaptive dictionary	341/51	341/106	
119	Digital communications equipment using differential quaternary frequency shift keying	375/133	375/135; 375/136; 380/34	
120	Analog memory system	365/45	365/183; 365/222; 365/230.06	
121	Wireless remote control and zoom system for a video display apparatus	345/671	345/158; 348/734	

	Title	Current OR	Current XRef	Retrieval Classif
122	Digital engine analyzer	345/24	324/394; 345/440.1; 345/440.2; 701/102; 73/117.3	
123	Method and apparatus for multiple access between transceivers in wireless communications using OFDM spread spectrum	375/260	375/219; 375/298; 380/34	
124	Digital engine analyzer	345/440.2	324/379; 701/102; 73/117.3	
125	Waveform peak capture circuit for digital engine analyzer	345/440.1	324/379; 701/102; 702/67	
126	Digital engine analyzer	345/440.1	324/121R; 324/379; 701/102; 702/67	
127	Enhanced accuracy delta-sigma A/D converter	341/143	341/158; 341/166	
128	Digital engine analyzer	345/440.1	324/121R; 324/379; 345/11; 345/169; 701/102	
129	Apparatus and method for recording image signals to allow reproduction at more than one data rate	386/109		
130	Digital data converter	375/247	341/77; 375/242; 375/248; 375/249	
131	Wireless remote control system for a television receiver	345/856	345/157; 345/860; 348/734; 398/1	
132	Dual band progressive television system with noise reduction	348/620	348/623	
133	Sequential scan system changes for multiple frequency range oscillator and control	315/364	348/536; 348/555; 348/735	
134	Video noise reduction system employing plural frequency bands	348/623	348/618; 348/908	

	Title	Current OR	Current XRef	Retrieval Classif
135	Picture coding and decoding apparatus using vector quantization	375/240.16		
136	Adaptive image acquisition system	382/313	382/221	
137	Analog memory for storing digital information	365/45	365/189.01; 365/222; 365/230.01; 968/900; 968/DIG.1	
138	Adaptive exposure control system	358/464	358/447; 358/471; 382/321	
139	Using infrared imaging to monitor and control welding	219/124.34	219/130.01; 219/130.21; 700/246; 700/249; 700/253; 700/259; 706/10; 706/49; 706/55; 706/904	
140	Stereophonic voice signal transmission system	381/1	379/202.01; 704/203; 704/212; 704/270; 704/278	
141	Multiple variable rate audio message playback	386/105		
142	Video/audio coded data recovery from a record medium	386/75	386/104	
143	Multiple variable rate audio message recording and playback	386/101	360/49; 360/72.2; 386/104	
144	Timing correction circuitry as for TV signal recursive filters	348/620	386/20	
145	Selectable video/audio coded data recovery from a record medium	386/104		
146	Data processor architecture	708/3	365/1; 365/222	
147	System for recording and playing back continuous-play and stop-motion signals	386/75	360/8; 386/101	
148	System for recording continuous-play and stop-motion signal	386/101	386/125	

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149	Analog memory for storing digital information	708/1	365/183; 365/45; 446/302	
150	Pattern recognition apparatus and method	348/243	358/461	
151	Analog read only memory	365/103	257/225; 257/235; 365/183; 365/45	
152	Electrical data pulse slicing	348/465	348/468; 375/317	
153	Memory system having servo compensation	365/45	365/222	
154	Automatic control of production machining by a machine tool	318/568.1	318/39; 318/571	
155	Automated radar data processing system	342/90	342/180; 342/182; 342/67; 342/93; 342/96; 342/97	
156	ADAPTIVE DATA MODULATOR	375/249	341/143	
157	ADAPTIVE THRESHOLD UNIT	250/336.1	250/338.1; 348/164	